

Amendments to the Specification

On page 1, after the title, please amend the paragraph added by the Preliminary Amendment Under 37 C.F.R. § 1.115 filed in the present patent application on August 26, 2003 as follows:

CROSS-REFERENCE TO RELATED APPLICATIONS

This application is a continuation of application no. 10/096,984, filed March 12, 2002, now allowed U.S. Patent No. 6,621,350, which is a divisional of application no. 09/692,656, filed October 9, 2000, now U.S. Patent No. 6,400,228, which claims priority to Great Britain application no. 0021438.7, filed August 31, 2000, now United Kingdom Patent No. 2,366,461.

Please amend the paragraph at page four, lines 8-21 as follows:

Referring to Figure 1, the operational amplifier block according to the present invention as shown in this embodiment is generally designated by reference numeral 10. The block comprises an operational amplifier 14 and a switch 34. For the purposes of describing the present invention in this example there is also provided a digital to analogue converter 12 and a logic gate 16. The digital to analogue converter 12 receives digital values on line 18, and generates an analogue signal on lines 20 and 22 to the positive and negative inputs of the operational amplifier 14 as is well known in the art. The operational amplifier 14 has a first

supply voltage terminal 36 connected to a node 38 33 of the switch 34. A second supply voltage terminal 24 of the operational amplifier 14 is connected to ground. The operational amplifier 14 generates amplified analogue signals on line 26. A switch 34 has two additional nodes 40 and 42. Node 40 is connected to a supply voltage V_{CCL} on line 30. Node 42 is connected to a supply voltage V_{CCH} which is connected to line 32. An output of a digital to analogue converter 12 on line 28 is provided to logic gate 16 which in turn provides a control signal on line 30 to the switch 34.

Please amend the paragraph at page four, lines 23-26 as follows:

As can be illustrated by the arrows within the switch unit 34, the switch 34 is controllable to connect node 38 33 to either the node 40 or 42. In this way the first supply terminal 36 of the operational amplifier receives either the supply voltage V_{CCL} on line 30 or the supply voltage of V_{CCH} on line 32.